

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sau Ching Wong  
Assignee: Multi Level Memory Technology  
Title: Efficient Redundancy System For Flash Memories With Uniformly  
Sized Blocks  
Serial No.: Not assigned yet Filing Date: June 30, 2003  
Examiner: Hur, Jung H. Group Art Unit: 2824  
Docket No.: MLM003US1D

San Jose, California  
June 30, 2003

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT UNDER CFR § 1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants call to the attention of the Examiner the documents listed on the attached substitute PTO Form-1449A.

Copies of the listed documents, other than those documents marked with an asterisk adjacent the cite no. in the substitute PTO Form-1449A, are enclosed. The documents marked with an asterisk were cited or submitted in U.S. Patent Application No. 09/927,693, filed on August 9, 2001, which the present application relies on for an earlier effective filing date under 35 U.S.C. 120.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

THE PATENT LAW OFFICES  
OF  
DAVID MILLERS  
6560 ASHFIELD COURT  
SAN JOSE, CA 95120  
(408) 927-6700  
FAX (408) 927-6701

No fee is believed required for submission of this information disclosure statement. However, the Commissioner is hereby authorized to charge any fees that may be required for consideration of this information disclosure statement to Deposit Account No. 50-1767.

EXPRESS MAIL LABEL NO.

EV 232 762 517 US

Respectfully submitted,



David Millers

Attorney for Applicants

Reg. No. 37,396

THE PATENT LAW OFFICES  
OF  
DAVID MILLERS

6560 ASHFIELD COURT  
SAN JOSE, CA 95120  
(408) 927-6700  
FAX (408) 927-6701

+

+

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

MLM003US1D

**Burden Hour Statement:** This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.

Please type a plus sign (+) inside this box → +

PTO/SB/08A (10-96)

Approved for use through 10/31/99. OMB 0651-0031  
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>		<b>Complete if Known</b>	
		Application Number	
		Filing Date	June 30, 2003
		First Named Inventor	Sau Ching Wong
		Group Art Unit	2824
		Examiner Name	Hur, J.
		Attorney Docket Number	MLM003US1D
Sheet	2	of	2

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	*6	Intel, "Preliminary Datasheet for 1.8 Volt Intel® Wireless Flash Memory (W18)," Order No. 290701-002 (January 2001) Pages i-79.	
	*7	Pathak, B. et al., 2001 IEEE International Solid-State Circuits Conference, Session 2, Non-volatile Memories, "2.3 A 1.8V 64Mb 100MHz Flexible Read-While Write Flash Memory" (Feb. 5, 2001).	

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.